

OFFICE OF THE DoRD

No. IITGOA/DoRD/16/REC/2024

Date: 29.10.2024

ADVERTISEMENT FOR JUNIOR RESEARCH FELLOW

Applications are invited for the position of Junior Research Fellow under the Project titled “**Debugging using Verification Techniques**” in the School of Mathematics and Computer Science, IIT Goa. The project is sponsored by the Science & Engineering Research Board (A statutory body of the Department of Science & Technology, Government of India).

<b>Nature of work</b>	Large language models (LLMs) are deep learning models that can be used for code processing and analysis. We shall use LLMs to identify bug locations in a buggy code. Later we shall leverage verification techniques to validate the findings of the LLMs.
<b>No. of position</b>	<b>One</b>
<b>Duration of appointment</b>	For a period of 1 Year. It may be further extended based on the performance.
<b>Eligibility</b>	<b>Essential qualification:</b> 1. B. Tech/B.E./B.Sc. in Computer Science (With qualified GATE). OR M.Tech/ME/ M.Sc in Computer Science (With qualified GATE).
<b>Consolidated salary</b>	Rs. 37,000/- Per Month
<b>HRA</b>	As per funding agency norms.
<b>Medical benefits</b>	As per the Institute norms.
<b>Leave entitlements</b>	As per the Institute norms.
<b>How to apply</b>	Interested candidates can fill out the below Google form. <a href="https://docs.google.com/forms/d/e/1FAIpQLSf8GOIq8IL7S0YiHgIJBpYMNbYKQUgZzY2CmdkKvxcenirCjA/viewform?usp=sf_link">https://docs.google.com/forms/d/e/1FAIpQLSf8GOIq8IL7S0YiHgIJBpYMNbYKQUgZzY2CmdkKvxcenirCjA/viewform?usp=sf_link</a>
<b>Last date of application</b>	<del>13.11.2024,5:00 PM.</del> 29.11.2024 ,5:00PM (For any update regarding the last date of submission of the application candidates have to visit <a href="https://iitgoa.ac.in/project- position/">https://iitgoa.ac.in/project- position/</a> )

**General Instructions:**

1. If the number of applications received in response to an advertisement is large, the constituted selection committee may restrict the number of candidates to be called for interview to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for an Interview.
2. If the applicants have any questions, they can write to [res-staff-rect@iitgoa.ac.in](mailto:res-staff-rect@iitgoa.ac.in) and in the subject they must mention the Institute project number “2023/EMR/SD/061”.
3. Informal inquiries via email may be made with PI: Dr. Sudakshina Dutta, Assistant Professor, School of Mathematics and Computer Science, via email at [sudakshina@iitgoa.ac.in](mailto:sudakshina@iitgoa.ac.in)
4. The appointment is purely temporary and will terminate automatically without any notice or compensation on the termination of the research project.

5. The appointed person shall have no claim of appointment / absorption in funding Agency or in IIT Goa.
6. The qualification prescribed should have been obtained from recognized Universities / Institutions.
7. No TA/DA will be admissible for appearing for the interview.
8. Selected candidates will have to join in a reasonable time upon receipt of the offer.

**Sd/-**  
**Assistant Registrar (R&D)**