

OFFICE OF THE DoRD

No. IITGoa/DoRD/09/REC/2023

Date: 12.04.2023

RECRUITMENT FOR JUNIOR RESEARCH FELLOW

Applications are invited for the position of Junior Research Fellow to work under the project titled “Study and realization of low Vpi Mach-Zehnder for OOK/BPSK/QPSK modulation up to 20GHz” in the School of Electrical Sciences, IIT Goa. The project is sponsored by Indian Space Research Organization, Department of Space, Government of India.

Nature of work	The project is to design, simulate, fabricate, and characterize electro-optic modulators used for optical communication transceivers. The research fellow is expected to have a sound understanding of integrated circuits and expertise of handling electronic design automation tools. Strong affinity towards circuits and systems for optical communication and photonic integrated circuits is desirable.
No. of Position	One (1)
Duration of Appointment	One year, which will be extended further based on the performance.
Eligibility	<p><u>Essential qualification:</u></p> <ul style="list-style-type: none"> ➤ Master’s degree in VLSI or allied branches <p style="text-align: center;">OR</p> <ul style="list-style-type: none"> ➤ Bachelor’s degree in Electronic & Communication or allied branches with a valid GATE score ➤ Qualified GATE SCORE in the relevant stream (Candidates with Bachelor’s degree must have a valid GATE Scorecard). <p><u>Desirable:</u></p> <ul style="list-style-type: none"> ➤ Experience in integrated circuit design software. ➤ Interest in photonic integrated circuits and optical communication.
Salary	31,000/- PM
DA	Not to be entitled to DA.
HRA	As per funding agency norms.
Medical Benefits	As per the Institute norms.
Leave entitlements	As per the Institute norms
How to apply	Interested candidates can fill the below form. https://forms.gle/JbYxGPVjM7FayGUB9
Last Date of application	27th April 2023, 5:00PM 8 th May 2023 5:00PM For any update regarding the last date of submission of application candidates must visit: https://iitgoa.ac.in/project-position/

General Instructions:

1. If the number of applications received in response to advertisement is large, the constituted selection committee may restrict the number of candidates to be called for selection to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for selection.
2. If the applicants have any questions, they can write to res-staff-rect@iitgoa.ac.in and in the subject they have to mention the Institute project number “2023/EMR/NN/056”.

3. Informal inquiries via email may be made with PI: Dr. Nandakumar N P, Assistant Professor, School of Electrical Sciences : npnandakumar@iitgoa.ac.in
4. No TA/DA will be admissible for appearing for the selection process.
5. Selected candidates will have to join in a reasonable time upon receipt of the offer.

Sd/-
Sr. Superintendent (R&D)