



OFFICE OF THE DoRD

No. IITGoa/DoRD/06/REC/2025

Date: 03.02.2025

ADVERTISEMENT FOR PROJECT ASSOCIATE –I

Applications are invited for the position of Project Associate-I to work under the project titled “Study and realization of low Vpi Mach-Zehnder for OOK/BPSK/QPSK modulation up to 20GHz” in the School of Electrical Sciences, IIT Goa. The project is sponsored by the Indian Space Research Organization, Department of Space, Government of India.

Nature of work	The project is to design, simulate, fabricate, and characterize electro-optic modulators used for optical communication transceivers. The project associate is expected to have a sound understanding of integrated circuits and expertise of handling electronic design automation tools. Strong affinity towards circuits and systems for optical communication and photonic integrated circuits is desirable.
No. of Position	One (1)
Duration of Appointment	One year, which will be extended further based on the performance.
Eligibility	<p>Essential Qualification:</p> <ul style="list-style-type: none"> ➤ Bachelor’s degree in Electronic & Communication or equivalent. <p style="text-align: center;">OR</p> <ul style="list-style-type: none"> ➤ Master of Science in Physics /Electronics or equivalent. <p>Desirable:</p> <ul style="list-style-type: none"> ➤ Experience in integrated circuit design software. ➤ Interest in photonic integrated circuits and optical communication.
Salary	31,000/- per month.
HRA	As per funding agency norms.
Medical Benefits	As per the Institute norms.
Leave entitlements	As per the Institute norms
How to apply	Interested candidates can fill out the below form. https://forms.gle/xwHs58BHN3f2CjNY7
Last date of application	18.02.2025,5:00 PM 26.02.2025 5:00 PM For any update regarding the last date of submission of the application, candidates must visit: https://iitgoa.ac.in/project-position/

General Instructions:

1. If the number of applications received in response to advertisement is large, the constituted selection committee may restrict the number of candidates to be called for selection to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for selection.
2. If the applicants have any questions, they can write to res-staff-rect@iitgoa.ac.in and in the subject they have to mention the Institute project number “2023/EMR/NN/056”.

3. Informal inquiries via email may be made with PI: Dr. Nandakumar N P, Associate Professor, School of Electrical Sciences: npnandakumar@iitgoa.ac.in
4. No TA/DA will be admissible for appearing for the selection process.
5. Selected candidates will have to join in a reasonable time upon receipt of the offer.

Sd/-
Assistant Registrar (R&D)