



Indian Institute of Technology Goa

भारतीय प्रौद्योगिकी संस्थान गोवा

At GEC Campus, Farmagudi, Ponda - 403 401, Goa

intel

Faculty Development Program on **SoC Design Methodology using Intel FPGAs**

Dates: 17th - 19th February 2022

Time: 9.30 am to 1.30 pm

Online Mode: MS Teams

Register Here!

Resource Persons

Israr Sheikh

F&E Manager, India, ASEAN and ANZ, Intel-PSG

Padmanaban K

Program Specialist –India, FPGA University Outreach,
Intel-PSG

Audience:

Faculty members and researchers from academic institutions

No Registration Fee

Maximum participants: 200

Last Date for Registration: 15th February 2022

e-certificates will be provided for all active participants

Requirements: Windows system with uninterrupted internet connection
Help for installing “Quartus Prime Lite” will be provided after registration

Day 1: Introduction to Intel FPGAs and Quartus

Day 2: Intel SoC FPGAs and SoC Design Flow

Day 3 : HLS and Intel FPGAs for AI

Contact:

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