



## OFFICE OF THE DoRD

No. DoRD/ADMIN/2021-22/033

Date: 23.03.2022

## RECRUITMENT FOR JUNIOR RESEARCH FELLOW

Applications are invited for the position of JRF under the Project titled “**Architect and Design an Approximate Processor for Image Processing Applications**” in the School of Electrical Sciences, IIT Goa. The project is sponsored by Science and Engineering Research Board under the Department of Science and Technology, Government of India.

<b>Nature of work</b>	Design an approximate Instruction Set Architecture (ISA) for RISC processor. Implement the ISA in VHDL, and Cadence tools. Test the final approximate processor design on Image Processing applications.
<b>No. of Position</b>	<b>One</b>
<b>Duration of appointment</b>	Initially for a period of 1 year. Appointment may be extended for further ten months based on the performance.
<b>Eligibility</b>	<p><b>Essential qualification:</b></p> <ul style="list-style-type: none"> <li>- M.E./M.Tech. in ECE/CS/IT with First Division.</li> <li>- Should have a valid GATE score.</li> </ul> <p><b>Desirable:</b></p> <ul style="list-style-type: none"> <li>-Expertise in VLSI Circuit/System Design.</li> <li>-Exposure in VHDL, and Cadence tools.</li> </ul>
<b>Salary</b>	Rs.31,000/- P.M.
<b>DA</b>	Not entitled to DA.
<b>HRA</b>	18 %
<b>Medical Benefits</b>	As per norms.
<b>Leave entitlements</b>	As per norms.
<b>How to apply</b>	Interested candidates can fill the below form. <a href="https://docs.google.com/forms/d/e/1FAIpQLSd648B5wzDBu52sLHAKIRNC-30pPmJT679UoP_nn1uhjoriTq/viewform?usp=sf_link">https://docs.google.com/forms/d/e/1FAIpQLSd648B5wzDBu52sLHAKIRNC-30pPmJT679UoP_nn1uhjoriTq/viewform?usp=sf_link</a>
<b>Last Date of application</b>	<del>06.04.2022, 5:00 PM.</del> 17.04.2022, 5:00 PM For any update regarding the last date of submission of application candidates have to visit <a href="https://iitgoa.ac.in/project-position/">https://iitgoa.ac.in/project-position/</a>

**Additional Instructions:**

1. If the number of applications received in response to advertisement is large. The constituted selection committee may restrict the number of candidates to be called for interview to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for Interview.
2. If the applicants have any questions, they can write to [res-staff-rect@iitgoa.ac.in](mailto:res-staff-rect@iitgoa.ac.in) and in the subject they have to mention the Institute project number “2022/EMR/SD/032”.
3. Informal inquiries via email may be made with PI- Dr. Sunil Dutt, School of Electrical Sciences on [sdutt@iitgoa.ac.in](mailto:sdutt@iitgoa.ac.in)
4. The appointment is purely temporary and will terminate automatically without any notice or on termination of the research project.
5. The appointed person shall have no claim of appointment / absorption in funding Agency or in IIT Goa.

6. The qualification prescribed should have been obtained from recognized Universities / Institutions.
7. No TA/DA will be admissible for appearing for the interview.
8. Selected candidates will have to join in a reasonable time upon receipt of the offer.

**Sd/-**  
**Assistant Registrar (R&D)**