

School of Electrical Sciences

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Syllabus for the PhD Written Test

The written test will comprise Part-A and Part-B. The test is of one and a half hours duration. Part-A is common to all. Part-B is specialization specific.

Syllabus for Part-A:

1. English grammar and style, numerical aptitude and spatial ability.
2. Linear algebra, differential equations and Fourier series.
3. Signals and systems, circuit analysis.
4. Digital electronics and analog circuits.

Syllabus for Part-B:

Communication and Signal Processing:

1. LTI Systems- Impulse response, frequency response, convolution, correlation.
2. Analysis of LTI Systems using Continuous and Discrete time Fourier transform, Z transform, Laplace transform.
3. Probability and Random Variables – Single random variable, Two random variables, probability density function (PDF), cumulative density function (CDF), joint density functions, marginal density functions, transformation of random variables, moment generating functions.
4. Random Process – stationarity, wide-sense stationarity, autocorrelation, power spectral density, joint power spectral densities, band-pass process and LTI system response to band-pass process.
5. Analog Communication – Amplitude and frequency modulation, circuits for modulators and demodulators, SNR performance.
6. Digital Communication – Sampling, quantization, Nyquist theorem, amplitude-shift-keying, phase-shift-keying modulation, frequency-shift-keying.

VLSI and Microelectronics:

1. Electronic Devices and Modeling: Energy bands semiconductors; drift and diffusion currents, mobility; Generation and recombination of carriers; diodes, BJT, MOS capacitor, MOSFETs and device models.
2. Digital circuits: Logic gates and their static CMOS implementations, combinational and Sequential circuits: latches, flip-flops, registers and counters.
3. Analog and Mixed-signal Circuits: Small signal equivalent circuits of diodes and MOSFETs; Simple diode circuits, current mirrors, biasing circuits, MOSFET amplifiers, Opamps: frequency compensation and stability, comparators, active filters (RC and Switched Capacitor), comparators, data converters: DACs and ADCs.

Power Engineering:

1. Electromagnetics- Electrostatics, magnetostatics, boundary conditions.
2. Electrical Machines- Magnetic circuits, Transformers, Rotating machines, DC motors: construction and operation, AC machines: Induction motor, Synchronous motor and generator.
3. Power Electronics – Switching circuits, Non-isolated DC/DC converters, Phase controlled Rectifiers, Single phase voltage source inverters.
4. Power System- Swing equation, Fault analysis, Transients in power systems, outdoor insulation.
5. Control systems- Modelling of dynamical system: in time domain and frequency domain, Time domain evaluation of systems with standard inputs, Stability analysis: Routh method, Root locus analysis, Bode plots.

Date of the PhD written test: 26th November, 2018.

Time of reporting: 8:30 A.M. (sharp) to Admin block, IIT Goa.

Venue of the written test: Will be informed at the Admin block.